

## AMD INSTINCT™ MI210 ACCELERATOR

## **Exascale-Class Technologies for the Data Center**

The AMD Instinct™ MI210 accelerator extends AMD industry performance leadership in accelerated compute for double precision (FP64) on PCIe® form factors for mainstream HPC and AI workloads in the data center. Label Built on AMD Exascale-class technologies with the 2nd Gen AMD CDNA™ architecture, the MI210 enables scientists and researchers to tackle our most pressing challenges from climate change to vaccine research. MI210 accelerators, combined with the AMD ROCm™ 5 software ecosystem, allow innovators to tap the power of HPC and AI data center PCIe® GPUs to accelerate their time to science and discovery.

#### AI & Machine Learning 200 50 180 45 160 4N 140 35 120 30 25 100 22.6 19 5 19 5 20 ۸n 60 15 10 40 97 5 20 AMPERE A100 PCIe® (300W) INSTINCT MI210 PCIe® (300W)

Superior Performance for HPC & Al

## Graph 1: Peak TFLOPS across range of mixed-precision Compute<sup>2</sup>

## Purpose-built Accelerators for HP & Al Workloads

Powered by the 2nd Gen AMD CDNA™ architecture, AMD Instinct™ MI210 accelerator delivers HPC performance leadership over existing competitive PCIe® data center GPUs today with up to a 2.3x advantage over Nvidia Ampere A100 GPUs in FP64 performance delivering exceptional performance for a broad set of HPC & AI applications.² The MI210 accelerator is built to accelerate deep learning training, providing an expanded range of mixed-precision capabilities based on the AMD Matrix Core Technology, and delivers an outstanding 181 teraflops peak theoretical FP16 and BF16 performance to bring users a powerful platform to fuel the convergence of HPC and AI.

## **Innovations Delivering Performance Leadership**

AMD innovations in architecture, packaging and integration are pushing the boundaries of computing by unifying the most important processors in the data center, the CPU, and the GPU accelerator. With our innovative double-precision Matrix Core capabilities along with the 3rd Gen AMD Infinity Architecture, AMD is delivering performance, efficiency and overall system throughput for HPC and AI using AMD EPYC™ CPUs and AMD Instinct™ MI210 accelerators.



## **Key Features**

PERFORMANCE	MI210	
Compute Units	104 CU	
Stream Processors	6,656	
Matrix Cores	416	
Peak FP64/FP32 Vector	22.6 TF	
Peak FP64/FP32 Matrix	45.3 TF	
Peak FP16/BF16	181.0 TF	
Peak INT4/INT8	181.0 TOPS	
MEMORY		
Memory Size	64GB HBM2e	
Memory Interface	4,096 bits	
Memory Clock	1.6GHz	
Memory Bandwidth	up to 1.6 TB/sec <sup>3</sup>	

RELIABILITY	MI210
ECC (Full-chip) RAS Support	Yes Yes
SCALABILITY	
Infinity Fabric <sup>™</sup> Links Coherency Enabled OS Support AMD ROCm <sup>™</sup> Compatible	up to 3 Yes (Dual   Quad Hives) Linux™ 64 Bit Yes
Board Form Factor Length Bus Interface SR-IOV Support Thermal Max Power Warranty	Full-Height, Full-Length (Dual Slot) 4.5" x 10.5" (11.43 CM x 26.67 CM) PCle® Gen4   Gen3 Support Yes (Passthrough Only) Passively Cooled 300W TDP (EPS12V, 8-pin) Three Year Limited <sup>5</sup>



### **Ecosystem without Borders**

AMD ROCm™ is an open software platform allowing researchers to tap the power of AMD Instinct™ accelerators to drive scientific discoveries. The ROCm platform is built on the foundation of open portability, supporting environments across multiple accelerator vendors and architectures. With ROCm 5, AMD extends its platform powering top HPC and AI applications with AMD Instinct™ MI200 series accelerators, increasing accessibility of ROCm for developers and delivering outstanding performance across key workloads.

#### HPC and MACHINE LEARNING APPLICATIONS









Energy













HPC

Life Sciences

Chemistry

Weather

Astrophysics

Automotive Reinforcement Image | Object | Learning

Video Detection & Classification

#### **OPEN PROGRAMING WITH CHOICE**

OpenMP | HIP | OpenCL™ | Python

### **OPEN FRAMEWORKS**

PyTorch | TensorFlow | ONNX | Kokkos | RAJA

#### **OPTIMIZED LIBRARIES**

BLAS | FFT | RNG | SPARSE | THRUST | MIOpen | RCCL

#### PROGRAMER AND SYSTEM TOOLS

Debuggers | Performance Analysis | System Management

### 2nd Generation AMD CNDA™ Architecture

The AMD Instinct™ MI210 accelerator brings commercial HPC & AI customers the compute engine selected for the first U.S. Exascale supercomputer. Powered by the 2nd Gen AMD CDNA™ architecture, the MI210 accelerator delivers outstanding performance for HPC and AI. The MI210 PCIe® GPU delivers superior double and single precision performance compared to the Nvidia Ampere A100 GPU for HPC workloads with up to 22.6 TFLOPS peak FP64 FP32 performance, enabling scientists and researchers around the globe to process HPC parallel codes more efficiently across several industries.1

AMD's Matrix Core technology delivers a broad range of mixed precision operations bringing you the ability to work with large models and enhance memory-bound operation performance for whatever combination of AI and machine learning workloads you need to deploy. The MI210 offers optimized BF16, INT4, INT8, FP16, FP32, and FP32 Matrix capabilities bringing you supercharged compute performance to meet all your AI system requirements. The AMD Instinct MI210 accelerator handles large data efficiently for training and delivers 181 teraflops of peak FP16 and bfloat16 floating-point performance for deep learning training.

## AMD Infinity Fabric™ Link Technology

AMD Instinct MI210 GPUs provide advanced I/O capabilities in standard off-the-shelf servers with our AMD Infinity Fabric™ technology and PCIe® Gen4 support. The MI210 GPU delivers 64 GB/s CPU to GPU bandwidth without the need for PCIe® switches, and up to 300 GB/s of Peer-to-Peer (P2P) bandwidth performance through three Infinity Fabric links. <sup>4</sup> The AMD Infinity Architecture enables platform designs with dual and quad, direct-connect, GPU hives with high-speed P2P connectivity and delivers up to 1.2 TB/s of total theoretical GPU bandwidth within a server design.4 Infinity Fabric helps unlock the promise of accelerated computing, enabling a quick and simple onramp for CPU codes to accelerated platforms.

### **Ultra-Fast HBM2e Memory**

AMD Instinct MI210 accelerators provide up to 64GB High-bandwidth HBM2e memory with ECC support at a clock rate of 1.6 GHz. and deliver an ultra-high 1.6 TB/s of memory bandwidth to help support your largest data sets and eliminate bottlenecks moving data in and out of memory.3 Combine this performance with the MI210's advanced Infinity Fabric I/O capabilities and you can push workloads closer to their full notential.

# **For More Information Visit:** AMD.com/INSTINCT | AMD.com/ROCm

1. World's fastest data center GPU is the AMD instinct" MI250X. Calculations conducted by AMD Performance Labs as of Sep 15, 2021, for the AMD instinct" MI250X (128GB HBMZe DAM module) accelerator at 1,700 MHz peak boost engine clock resulted in 95,7 TFLOPS peak theoretical double precision (FP64), 95,7 TFLOPS peak theoretical single precision (FP64), 95,7 TFLOPS peak theoretical single precision (FP32), 383,0 TFLOPS peak theoretical single precis

2. Calculations conducted by AMD Performance Labs as of Jan 14, 2022, for the AMD Instinct\* MIZIO (64GB HBMZe PCIe® card) accelerator at 1,700 MHz peak boost engine clock resulted in 45.3 TFLOPS peak theoretical double precision (FP64 Matrix), 22.6 TFLOPS peak theoretical double precision (FP64, Matrix), 22.6

3. Calculations conducted by AMD Performance Labs as of Jan 27, 2022, for the AMD Instinct\* MI210 (64GB HBM2e) accelerator (PCIe\*) designed with AMD CDNA\* 2 architecture 6nm FinFet process technology at 1,600 MHz peak memory clock resulted in 64 GB HBM2e memory capacity and 1,6384 TB/s ((3.20 Cbps\*(6,096 bits))/8). Calculations conducted by AMD Performance. MI210 memory bus interface is 4,096 bits and memory data rate is 3.20 Cbps for total memory bandwidth of 1,6384 TB/s ((3.20 Cbps\*(6,096 bits))/8). Calculations conducted by AMD Performance Labs as of Sep 18, 2020, for the AMD Instinct\* MI010 (2566 BHM2) accelerator (PCIe\*) designed with AMD CDNA\* architecture. The FinFet process technology at 1,502 MHz peak clock resulted in 32 GB HBM2 memory capacity and 1,2288 TFLOPS peak theoretical memory bandwidth performance. MI210 memory bus interface is 4,096 bits and memory data rate is 2.40 Cbps for total memory bandwidth of 1,2288 TB/s ((2.40 Cbps\*(4,096 bits))/8). MI200-42

Interlates 14,030 bits and memory data at the SZAU OUDS post of total memory darknowth on L226 in 1975 (K2AU OUDS) (K2AU OUDS)

S. The AMD Instinct" accelerator products come with a three-year limited warranty. Please visit www.AMD.com/warranty page for warranty details on the specific graphics products purchased. Toll-free phone service available in the U.S. and Canada only, email access is global @2022 Advanced Micro Devices, Inc. All rights reserved. AMD, the AMD Arrow logo, AMD Instinct, Infinity Fabric, ROCm, and combinations thereof are trademarks of Advanced Micro Devices, Inc. PCIe is a registered trademark of PCI-SIG Corporation. OpenCL" is a registered trademark of the Under Increase by Khronos. The OpenMP name and the OpenMP logo are registered trademarks of the OpenMP Architecture Review Board. Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies. We thank the Computational Infrastructure for Geodynamics (Kirty/Jegodynamics.org) which is funded by the National Science Foundation under awards EAR-

